

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: Mukunda Krishnappa et al.

Filed: December 15, 2003

For: Apparatus and Methods for Configuration of Programmable Logic  
Devices

Serial No.: 10/736,167

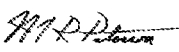
Group Art Unit: 2819

Examiner: Chang, D.

Att'y Docket No.: ALTR:024

Pursuant to 37 C.F.R. § 1.8, I certify that this correspondence is being filed electronically, via facsimile, or deposited with the U.S. Postal Service in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

July 29, 2006  
Date

  
Name

**REQUEST FOR AN EXTENSION OF TIME**

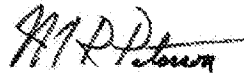
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136(a), Applicants petition for an extension of time for one month(s) to and including July 21, 2006, in which to respond to the Office Action dated March 21, 2006.

Concurrently with filing this paper, the Applicant provides payment electronically. If any other fees are due for any of the enclosed materials, please deduct such fees from (or credit any overpayment to) deposit account number 50-3813/ALTR-023.

Respectfully submitted,



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MAXIMILIAN R. PETERSON  
Registration No. 46,469  
Attorney for the Applicant

LAW OFFICES OF MAXIMILIAN R. PETERSON  
P.O. Box 93005  
Austin, Texas 78709-3005  
Phone: 512-382-4404  
Fax: 512-382-4405